

INTER-CHIP INTERFERENCE REDUCTION FOR CDMA WIRELESS COMMUNICATIONS

ABSTRACT OF THE INVENTION

A digital CDMA wireless communication system. The system includes one
5 or more of said transmitters with a base station baseband processor, a finite impulse
response (FIR) filter, a pre-distortion phase equalizer and a digital-to-analog (DAC)
converter. Receivers have an analog-to-digital (ADC) converter, a FIR filter, a
phase equalizer and a receiver baseband processor. The receiver FIR filter is
10 matched to the transmitter FIR filter and the receiver phase equalizer is matched to
the pre-distortion phase equalizer.

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